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Inoue, T.; Kumar Das, D.; Sano, C.; Mihara, T.; Fujiwara, H.;
Computer Aided Design, 2000. ICCAD-2000. IEEE/ACM International
Conference on , 5-9 Nov. 2000
Page(s): 550 -556

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Das Gupta, S.; Al-Musawi, M.J.;
Reliability, IEEE Transactions on , Volume: 37 Issue: 1 , April 1988
Page(s): 75 -80

[\[Abstract\]](#) [\[PDF Full-Text \(464 KB\)\]](#) **IEEE JNL****3 Synthesis of symmetric functions for path-delay fault testability**

Chakrabarti, S.; Das, S.; Das, D.K.; Bhattacharya, B.B.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE
Transactions on , Volume: 19 Issue: 9 , Sept. 2000
Page(s): 1076 -1081

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designs using net regularity extraction**

Das, S.; Khatri, S.P.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE
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